



NuProgPlus Series User Manual

Universal Programmer and Duplicator

Model: NuProgPlus-U8/NuProgPlus-U16 Please read the instructions carefully before use.

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I. Introduction

This manual will be focusing on the hardware specification and the software quick guide of NuProgPlus-U8/U16. If you would like to know more about the DediWare software, please download it from DediProg website. https://www.dediprog.com/download

II. Product Information

| Model Functions | ProgMaster-U8 | NuProgPlus-U8 | NuProgPlus-U16 |
|--|-------------------------------------|---|-----------------|
| Supported IC Kinds | Universal (UFS is not supported) | Universa | l (Support UFS) |
| USB Interface | USB2.0 | ι | JSB3.0 |
| LCD Keypad Panel | V | | \checkmark |
| Sites | 8 Sites | | 8 Sites |
| Socket | 8 Sockets | 8 Sockets (16 Sockets for SPI Flash, eMMC, and UFS) | |
| Power/Pass/Busy/Erro r LED Indicators | V | | \checkmark |
| Start button | V | | V |
| Multiple Programmers* | V | | V |

Note * Multiple programmers of the same model can be driven by 1 PC

III. System Requirement

| CPU: | i5 or above |
|------------------|---|
| OS: | Windows 7/8.1/10 |
| USB Port: | USB 3.0 |
| Free Disk Space: | More than 256GB (Depends on the image file size) |
| CD ROM: | It is necessary for installing the software |

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IV. Product Descriptions













A. Power and System Status LED Indicators

The System Error LED will turn off after the initialization is done.

B. Start Button

Use Start button to start programming in production mode.

C. Status LED Indicator

Red (Error): Programming fail

Yellow (Busy): In the progress of programming Green (Pass): Program succeeds

D. LCD Keypad Panel

When the programmer is in the Stand-alone mode, the user can operate programming through the LCD monitor and keypad.

E. Socket Sites

Install the socket adaptor according to different types of IC package.

F. ESD & GND

GND is for programming grounded; ESD is for connecting ESD wrist strap.

G. USB Interface

Control the DediWare software and the programmer by using the USB3.0 connector, one end connects to the programmer; the other end connects to the USB3.0 port on the computer

H. Power Connector

Connect with the DC 15V power cable provided by Dediprog (Input 100~240V/50~60Hz; Output 15V)

I. Power Switch



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DediWare Quick Installation V.

The software is provided with the purchase of NuProgPlus-U8/U16. The latest version is available on our website. www.dediprog.com

5.1 Software Installation

1. Install DediWare and driver



| 😽 DediProg 3.16.7.3 Setup | | | 8 | | X |
|--|---|----------------|---|--|----------|
| Choose Components | | | | | |
| Choose which features of Dedi | Prog 3.16.7.3 you war | nt to install. | 63 | | 15 |
| Check the components you wa install. Click Install to start the | nt to install and uncheo installation. | :k the comp | ponents you do | n't want to | D |
| Select components to install: | DediWare GUI DediWare kernal USB Driver Firmware User Manual Chip Database Microsoft Visual 0 | C++ 3Q1! | Description Install Micro C++ 2015- Redistribut: - 14.28.293 automatical | osoft Visua 2019 able(x64/x 225 ly | l 86] |
| Space required: 656.2MB | < | ۍ ۲ | | | |
|) DediWare Installation system | | | | | 1 |
| | | | | | |

- > The listed items are expected to be installed; some items can be canceled.
- > Pointing the cursor on the item, it will show the description on the right side.
- If the PC has not been installed any versions of the DediWare previously, then it is required to install the Chip Database.





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2. After installation, three DediWare icons will appear on the desktop. Administrator permission is required to execute the software. If user account is not administrator level, please enable the option "Run this program as an administrator" in the software shortcut.

| General Ded Ware General | General Client Run the software for MCU/Flash/EEPROM programming. |
|--------------------------------|---|
| | eMMC_UFS Client Run the software for eMMC and UFS programming and duplicating. |
| DecilWare CLI | Command Line Interface Supporting the Command line of the StarProg Series Programmers (Please refer to the DediWare Command Line User Manual). |

5.2 Install NuProgPlus-U8/U16 Programmer

- 1. Connect the power cable to the NuProgPlus-U8/U16 programmer
- 2. Connect the USB cable to the NuProgPlus-U8/U16 programmer
- 3. Install the socket adaptor, please refer to VI. Socket Adaptor Installation
- 4. Turn on the power of the programmer
- 5. Place IC into the socket adaptor
- 6. Open the software and start programming

5.3 Engineering mode for eMMC UFS Client (For eMMC and UFS IC only)

Engineering Mode Functions



Detect: Select IC type to detect or enter the part number manually. (It is recommended to use Detect first.) Load: Load the programming file and settings.

Copy / Scan: Not applicable for NuProgPlus-U8/U16.

Buffer: Show the content of the partition.

Config: IC Register Programming settings and Batch settings.

Save Prj: Save as a project folder.

Load Prj: Import the project folder.



ReadIC: Read the contents of the partition and the Register.
Blank: Blank check.
Wipe: Delete the Partition data.
Program: Program the Partition and the Register.
Verify: Verify the Partition and the Register.

Auto Batch: Run the batch.

5.3.1 eMMC Programming and Settings

1. Click Detect > eMMC



- After detecting the IC's ID successfully, it will show the model of the eMMC IC and the current Partition quantity and volume in the Log.
- It will show the related information of the eMMC IC and its applicable socket adaptor part number in the ChipInfo section at the bottom.
- If the detection has failed.
 - Please check the IC pin 1 position.
 - Please check if the selected socket has been placed an IC correctly.
 - Please check the IC ball condition to avoid contact fail, or replace with other IC.



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2. Load the programming file: Assign the Partition and the programming file for programming. It is required to have administrator permission to execute the software, and if the user account does not have the permission, please open it with "Run as administrator" manually.



- File1: It is the configuration tab of the first programming file, you can click "+" to add other tabs for \geq other files. If you would like to delete the unnecessary tabs, just right click on the tab twice.
- FilePath: It is the file path and the file name; you can use "..." to search the file. If the file in on the \geq external PC, then please create a network drive and use it to open the file. It does not support IP or shortcut methods.
- FileFormat: eMMC can only read by binary method. \geq
- \triangleright FileChecksum: The calculation method for file Checksum.
- \geq FileOffset: The starting address reading from the file (hexadecimal).
- \triangleright Skip Blank Value: It will skip the empty area in programming process, which will help to reduce the programming time (recommended).
- **PartitionName:** The Partition that needs to be programmed. \geq
- \triangleright SectorIndex: The programming starting sector index of the eMMC IC (Decimal).
- \triangleright **SectorCout:** It is the accounts for the sectors of the programming file (Decimal).
- Show Images: List the configurations of all the programming files. \geq

If you need to activate the EUDA (Enhanced User Data Area) or the GPP0~3 (General Purpose Partitions), please complete the related configuration settings on ExtCSD first, and then after writing, use the Detect IC to get the new Partition(s) configuration again.



3. If needed, you can set up the Register on ExtCSD in IC Config.

| 🚽 Conf | ig extCSD | | | | | | |
|--------|-----------|-----------------------------|-------|---------------------|--------------------|---|-----------|
| | | | | | | | |
| Batch | extCSD | | | | | | |
| | extCSD | | | | | | |
| extCSD | BYTES | Name | Туре | Chip Value (Hex) | Buffer Value (Hex) | - | Check Sel |
| - 83 | [175] | ERASE_GROUP_DEF | R/W/E | | 00 | | Unchack |
| RPMB | [139:136] | ENH_START_ADDR | R/W | | 00000000 | | Uncheck . |
| -07 | [142:140] | ENH_SIZE_MULT | R/W | | 000000 | | Check A |
| | [145:143] | GP_SIZE_MULT_1 | R/W | | 000000 | | Unckeck A |
| | [148:146] | GP_SIZE_MULT_2 | R/W | | 000000 | | |
| | [151:149] | GP_SIZE_MULT_3 | R/W | | 000000 | | |
| | [154:152] | GP_SIZE_MULT_4 | R/W | | 000000 | | |
| | [156] | PARTITIONS_ATTRIBUTE | R/W | | 00 | | |
| | [160] | PARTITIONING_SUPPORT | R | | 00 | | |
| | [155] | PARTITION_SETTING_COMPLETED | R/W | | 00 | | |
| | [17] | PRODUCT_STATE_AWARENESS_ENA | R/W/E | | 00 | | |
| | [25:22] | PRE_LOADING_DATA_SIZE | R/W/E | | 0000000 | | |
| | [133] | PRODUCTION_STATE_AWARENESS | R/W/E | | 00 | | |
| | [183] | BUS_WIDTH | W/E_P | | 00 | | |
| | [185] | HS_TIMING | R/W/E | | 00 | | |
| | [192] | EXT_CSD_REV | R | | 00 | | |
| | [196] | CSD_STRUCTURE | R | | 00 | | |

- Check the needed items for programming and set up the values.
- Before setting up the values, please delete all the previous values first.
- It will not save the unchecked items.
- Check Selected: Click on anywhere in the column that you want to select, and then it will check all the boxes at once after you click Check Select
- Uncheck Selected: Click on anywhere in the column that you want to cancel, and then it will cancel the boxes at once after you click Uncheck Selected.
- > Check All: Check all the columns at once.
- Uncheck All: Cancel all the columns at once.
- Some registers can only be written for once, after it has been written successfully, then it will not be able to change again. If you have any concerns regarding each Register's values of the eMMC ExtCSD, please get the correct values from the IC supplier.
- ▶ If Register 134~156 need to be programmed, Register 175 must be set to 1.



4. Set up the Batch Operation.

| | Config | PROJ | PROJ | Copy Function | | _ | R | | |
|-----------------------------------|--------|--|--|---|--------------|------------|--------|--------|---|
| Config | | | | | | | | | × |
| Batch | Batch | Setting | | | | | | | |
| Batch extCSD extCSD RPMB | | Batch C extCSD Erase chip Blank check(En Program chip Checksum veril Checksum veril Blank check Batch Setting None PSA-Autoi PSA-Manu | peration gModeOnly) 'y D the partition which EXT (CopyModeOnly)(ia(CopyModeOnly) | <pre>ch not loaded file while doi (EPV) y)(EPV)</pre> | ng verificat | tion(Verif | y All) | ed | |
| c. | | | | | | | | | |
| | | | | | | | OK | Cancel | |

Basic Options

- > **extCSD:** Program and verify ExtCSD register.
- **Erase Chip:** Delete all the Partition data.
- Blank check (EngModeOnly): Blank check and it can only be executed in the engineering mode.
- **Program chip:** Program the Partition.
- **Checksum verify:** Verify the Partition.

Advance Options

- Skip Check ID: Not execute ID check
- PSA-Auto: Activate PSA-Auto programming function. The Batch mode will be fixed to EPV, and only can run in the copy mode.
- PSA-Manual: Activate PSA-Manual programming function. The Batch mode will be fixed to EPV, and only can run in the copy mode.
- If you have any concerns about the PSA application methods and conditions of the eMMC, please contact with the IC supplier.



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5. Save as the project folder.

| Detect | . 🔀 Load | Copy | Scan | Buffer | Config | PROJ Save Prj | PROJ Load Prj | Copy Function | Stop | R Start Copy | |
|-------------------|--------------|--------|------------------|---------|------------|------------------|------------------|--------------------------|----------|--------------------|-------|
| 45 ReadIC | 🕌 🗸 Blank | Wipe | ▼ 🐇 ▼ Program | Verify | Atl Save N | NuProg+ Pro | ject (Name a f | older) | | > | × |
| | | | | | Lo | ocation: D | ESKTOP\ | | | ✓ ↑ | |
| 11 NuPro | ogPlus | Site # | 1A . | Site | #1 | N | Name | | Size | Date Modified |] _ m |
| GA Ver: W Ver: | 1F 1.1.19 | Pass: | 0 | Pass | : C | | QOBC1153K3/ | A1_M1B_B - V1.0 (0xee888 | 145KB | 2019/12/26 09:28 | i i i |
| N: | NPS00001 | Fail: | o IDL | E Fail: | C | PC | R5F56514_32 | 82701-7_DQD0A_CP2E-S_E | 512KB | 2019/12/13 03:13 | |
| BLink | Start ≑ | N | 0.0% | | _ | | SiTime Time Ma | achine.Ink | 2KB | 2019/06/06 11:54 | |
| | | | | | | | SLF5.297.041 | CX B0.dprj | 10240KB | 2019/12/11 01:30 | |
| | 0.0% | Site # | 1B 🚽 | Site | : #1 De | esktop | STM32WB55.c | loc | 28KB | 2019/12/11 10:33 | l lir |
| | | Pass: | 0 | Pass | : 0 | | teraterm.log | | 2KB | 2019/12/23 11:58 | |
| | | Fail: | 0 N/A | Fail: | C | | ufe vol | | 2150-#KB | 2019/12/20 11:33 | |
| | | N | 0.0% | N | _ | | | PEX 0 3 txt | 35KB | 2019/12/24 02:19 | |
| | | | | | 2 | | WeChat 圖片 | 20191223170909.jpg | 65KB | 2019/12/23 05:09 | - DF |
| | | Site # | 5A . | Site | # | | Western Digita | I INAND MC EU331 Data Sh | 1011KB | 2019/12/16 05:49 | - Fil |
| | | Pass: | 0 | Pass | : C | | XL710_5p05_9 | FI_MCTP_CFGID7p2_Quad | 4096KB | 2019/12/26 09:00 | |
| | | Fail: | 0 < N/A | Fail: | C | | Zoom.lnk | | 1KB | 2019/11/20 09:12 | |
| | | N | 0.0% | | | |] 產品功能比較 | 表.xlsx | 13KB | 2017/11/21 01:34 🗸 | |
| | | | | N | | | c | | | > | Ba |
| | | Site # | 5B | Site | # | F | leName: | eMMC_test | | ~ ОК | |
| | | Pass: | 0 | Pass | : C | | - | | | | |

Name the project and it will generate a project folder after clicking OK, which will save all the required settings and content for programming. Please name the project by English character, number and underscore (_), and do not use space or other symbols.

| | > | еM | MC_test | | ٽ ~ | 搜尋 eM , P |
|----|---------------|----|---------------------|---------------------|----------|------------|
| | | ^ | 名稱 ^ | 修改日期 | 類型 | 大小 |
| | | | cart_eMMC_2550.bin | 2019/12/27 下午 07:13 | BIN 檔案 | 3,737 KB |
| | 7 | | eMMC_test.npprj | 2019/12/27 下午 07:13 | NPPRJ 檔案 | 24 KB |
| | A | | NuProgPlus_eMMC.vec | 2019/12/27 下午 07:13 | VEC 檔案 | 4 KB |
| | A | | UserArea.dpdat | 2019/12/27 下午 07:13 | DPDAT 檔案 | 262,144 KB |
|)7 | \mathcal{A} | | UserArea.dpmap | 2019/12/27 下午 07:13 | DPMAP 檔案 | 4 KB |
| | * | | | | | |
| | | | | | | |



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5.3.2 UFS Program and Settings

1. Click Detect > UFS



- After detecting the IC's ID successfully, it will show the internal ID of the UFS IC and the current Partition quantity and volume in the Log.
- It will show the related information of the UFS IC and its applicable socket adaptor part number in the ChipInfo section at the bottom.
- If the detection has failed.
 - Please check the IC pin 1 position.
 - Please check if the selected socket has been placed an IC correctly.
 - Please check the IC ball condition to avoid contact fail, or replace with other IC.



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2. Load the programming file: Assign the Partition and the programming file for programming.



- File1: It is the configuration tab of the first programming file, you can click "+" to add other tabs for other files. If you would like to delete the unnecessary tabs, just right click on the tab twice.
- FilePath: It is the file path and the file name; you can use "…" to search the file. If the file in on the external PC, then please create a network drive and use it to open the file. It does not support IP or shortcut methods.
- FileFormat: UFS can only read by binary method
- **FileChecksum:** The calculation method for file Checksum.
- FileOffset: The starting address reading from the file (hexadecimal)
- Skip Blank Value: It will skip the empty area in programming process, which will help to reduce the programming time (recommended).
- > PartitionName: The Partition that needs to be programmed
- SectorIndex: The programming starting sector index of the UFS IC (Decimal).
- SectorCout: It is the accounts for the sectors of the programming file (Decimal).
- Show Images: List the configurations of all the programming files.

If there is no available Partition(s) for the UFS IC to set up the programming file, then please complete Partition(s) configuration settings, and after writing, use Detect IC to get new Partition(s) configuration again.

Set up the UFS Partition(s) configuration according to the UFS IC datasheet in the Configuration Descriptor. If you have any concerns regarding the UFS Configuration Descriptor values, please get the correct value from the IC supplier.



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3. If needed, you can set up the Registers of the Descriptor/Attribute/Flag on ExtCSD in IC Config.

| Base Chip Value (Hex) Buffer Value (Hex) Unit Unit Unit Will Buffer Valia Description/DN Buffer Value (Hex) Unit Buffer Value (Hex) Buffer Value | Header Header BYTES Name Chi | | | 🗌 Ena | ble | Null Buffer | | | | |
|--|------------------------------|----------------------------------|--------|-------|---------|-------------------|------------------|--------------|--------|-----|
| Bytes Name Chip Value (Hex) Buffer Value (Hex) Unit Image: Stress of the stress of th | Attra BYTES Name Chi | | | | | | | LUN | 0 ~ | |
| Image: Second | ATTC: DITES Nume em | nn Value (Hey) – Butter Value (H | (Hev) | Unit | BVTES | Name | Chip Value (Hev) | Buffer Value | (Hev) | CIO |
| Image: Second | [Ox0] bl ength | 90 | (TIEX) | | I0x101 | bi UEnable | chip value (Hex) | 00 | (TIEX) | |
| Image: Second continue One | [0x1] bDescriptorIDN | 01 | | 0 | [0x11] | bBootLunID | | 00 | ~ | |
| Image: Non-Structure Image: No | Flag. [0x2] bConfDescContin | 00 | | 0 | [0x12] | bLUWriteProtect | | 00 | ~ | |
| Image: Non-State in the image: | [0x3] bBootEnable | 00 | ~ | 0 | [0x13] | bMemoryType | | 00 | ~ | |
| Image: Non-State Image: Non-State< | [0x4] bDescrAccessEn | 00 | ~ | 0 | [0x14:0 | dNumAllocUnits | | 00000000 | | |
| [0x6] bHighPriorityLUN 00 V 0 [0x19] bLogicalBlockSize 0C V | [0x5] blnitPowerMode | 00 | ~ | 0 | [0×18] | bDataReliability | | 00 | ~ | |
| | [0x6] bHighPriorityLUN | 00 | ~ | 0 | [0×19] | bLogicalBlockSize | | 0C | ~ | |
| [0x7] bSecureRemoval 00 | [0x7] bSecureRemoval | 00 | ~ | 0 | [0x1A] | bProvisioningType | | 00 | ~ | |
| [0x8] blnitActivelCCLevel 00 | [0x8] blnitActiveICCLevel | 00 | ~ | 0 | [0x1B:0 | wContextCapabili | | 0000 | | |
| [0x9:0x wPeriodicRTCUp 0000 0 [0:10: Reserved 000000] | | 0000 | | 0 | [0x1D: | Reserved | | 000000 | | |
| (0x8.0x5) Researed | [0x9:0x wPeriodicRTCUp | 0000 | | | | | | | | |

- Configuration Descriptor are for the UFS IC Partition(s) configurations, it can set up maximum of eight \geq partitions (LUN0~LUN7)
- Attribute and Flag are for setting up the UFS IC attributes. \geq
- \triangleright Some registers can only be written for once, after it has been written successfully, then it will not be able to change again. If you have any concerns regarding each Register's values of the UFS Descriptor/Attribute/Flag, please get the correct values from the IC supplier.



Configuration Descriptor

| | | | | 🗹 Ena | able | Null Buffer | | LUN 0 | |
|-------------------|---------------------|------------------|--------------------|-------|---------|-------------------|------------------|--------------------|---|
| leader - BYTES | Name | Chip Value (Hex) | Buffer Value (Hex) | Unit | BYTES | Name | Chip Value (Hex) | Buffer Value (Hex) | |
| 0x0] | bLength | 90 | 90 | 0 | [0x10] | bLUEnable | 01 | 01 | ~ |
| Dx1] | bDescriptorIDN | 01 | 01 | 0 | [0x11] | bBootLunID | 00 | 00 | ~ |
|)x2] | bConfDescContin | 00 | 00 | 0 | [0x12] | bLUWriteProtect | 00 | 00 | ~ |
| Dx3] | bBootEnable | 01 | 01 ~ | 0 | [0x13] | bMemoryType | 00 | 00 | ~ |
|)x4] | bDescrAccessEn | 00 | 00 ~ | 0 | [0x14:0 | dNumAllocUnits | 00007740 | 00007740 | |
|)x5] | blnitPowerMode | 01 | 01 ~ | 0 | [0x18] | bDataReliability | 00 | 00 | ~ |
| 0x6] | bHighPriorityLUN | 05 | 05 ~ | • 0 | [0x19] | bLogicalBlockSize | 0C | 0C | ~ |
| 0x7] | bSecureRemoval | 00 | 00 ~ | 0 | [0x1A] | bProvisioningType | 00 | 00 | ~ |
| 0x8] | blnitActivelCCLevel | 00 | 00 ~ | • 0 | [0x1B:0 | wContextCapabili | 0000 | 0000 | |
| 0x9:0x | wPeriodicRTCUp | 0000 | 0000 | 0 | [0x1D: | Reserved | 000000 | 000000 | |
| 0xB:0xF] | Reserved | 000000000 | 000000000 | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |

- **Read:** Read the values from the IC and show on the Chip Value column.
- Clone: It will copy the values in the Chip value column to the corresponding Buffer Value column.
- Enable: It will save the settings for the Buffer Value.
- > Null Buffer: It means the software has not saved any settings yet.
- **LUN:** Choose the LUN.
- Before setting, please execute Enable, Read and Clone in order, and then change the fields that need to be adjusted.
- > After settings, it will only be activated after running Program Descriptor.



File function can save the Buffer Values as CSV file, or import the CSV file to the Buffer Value.



Attributes

| latch | Att | ribute File | | | | | | | |
|----------|-----|---|------|---------|--------------------------------------|--------------|--|--------|------------|
| 0 | IDN | Name | Туре | SIZE | Chip Value (Byte:Hex, Bit:Binary) | v | Buffer Value (Byte:Hex, Bit:Binary) | | Check |
| | 0 | bBootLunEn | R/W | 1 Byte | 1 | \checkmark | 01(h): Enabled boot from Boot LU A | \sim | Uncheck |
| 22 | 2 | bCurrentPowerMode | RO | 1 Byte | 11 | | 11(h): Active power mode | \sim | Check All |
| - C. C. | 3 | bActivelCCLevel | R/W | 1 Byte | 0 | \checkmark | 00(h): Lowest Active ICC level | \sim | |
| ttr | 4 | bOutOfOrderDataEn | R/WO | 1 Byte | 0 | \checkmark | 00(h): Out-of-order data transfer is disabled. | \sim | Uncheck Al |
| 222 | 5 | bBackgroundOpStatus | R | 1 Byte | 0 | | 00(h): Not Required | \sim | |
| <u> </u> | 6 | bPurgeStatus | RO | 1 Byte | 0 | | 00(h): Idle | \sim | |
| lag | 7 | bMaxDataInSize | R/W | 1 Byte | 8 | \checkmark | 8 | | Read |
| 02 | 8 | bMaxDataOutSize | R/W | 1 Byte | 20 | \checkmark | 20 | | Clone |
| | 9 | dDynCapNeeded | RO | 4 Bytes | 1 | | 1 | | |
| PMB | 10 | bRefClkFreq | R/WO | 1 Byte | 1 | \checkmark | 01(h): 26 MHz | \sim | |
| 103 | 11 | bConfigDescrLock | R/WO | 1 Byte | 1 | \checkmark | 01(h): Configuration Descriptor locked | \sim | |
| | 12 | bMaxNumOfRTT | R/W | 1 Byte | 1 | \checkmark | 1 | | |
| | 13 | wExceptionEventControl | R/W | 2 Bytes | 1 | \checkmark | 1 | | |
| | 14 | wExceptionEventStatus | RO | 2 Bytes | 1 | | 1 | | |
| | 15 | dSecondsPassed | W | 4 Bytes | | \checkmark | 0000000 | | |
| | 16 | wContextConf[15:8]:Reserved | R | 8 Bits | 0000000 | | 0000000 | | |
| | 16 | wContextConf[7:6]:Reliability mode | R/W | 2 Bits | 0 | \checkmark | 00(b) | \sim | |
| | 16 | wContextConf[5:3]:Large Unit multiplier | R/W | 3 Bits | 0 | \checkmark | 000(b) | \sim | |
| | 16 | wContextConf[2]:Large Unit context | R/W | 1 Bit | 0 | \checkmark | 0(b) | \sim | |
| | 16 | wContextConf[1:0]:Activation and direction mode | R/W | 2 Bits | 1 | \checkmark | 01(b) | \sim | |
| | 17 | dCorrPrgBlkNum | R | 4 Bytes | 1 | | 1 | | |

- Check the needed items for programming and set up the values.
- Before setting up the values, please delete all the previous values first.
- The unchecked items in the Buffer value setting will not be saved.
- Check Selected: Click on anywhere in the column that you want to select, and then it will check all the boxes at once after you click Check Select.
- Uncheck Selected: Click on anywhere in the column that you want to cancel, and then it will cancel the boxes at once after you click Uncheck Selected.
- > Check All: Check all the columns at once.
- > Uncheck All: Cancel all the columns at once.
- **Read:** Read the values from the IC and show on the Chip Value column.
- Clone: It will copy the values in the Chip value column to the corresponding Buffer Value column.
- Partial columns are set up by bit method.

| Attr | ibute | File |
|--------------|--------|--------|
| IDN (Hex) | Name | E |
| 0 | bBoot | unEn |
| 2 | bCurre | ntPowe |

File function can save the Buffer Values as CSV file, or import the CSV file to the Buffer Value.



Flag

| Flag | File | | | | | | |
|------|----------------------|------|------------|--------------|---|--------|-------------|
| IDN | Name | Туре | Chip Value | V | Buffer Value | | Check |
| . 1 | fDeviceInit | R/W | 0 | \checkmark | 0: Device initialization completed or not started yet. | \sim | Unabarda |
| 2 | fPermanentWPEn | R/WO | 0 | \checkmark | 0: Permanent write protection disabled. | \sim | Uncheck |
| 3 | fPowerOnWPEn | R/W | 0 | \checkmark | 0: Power on write protection disabled. | \sim | Check All |
| 4 | fBackgroundOpsEn | R/W | 1 | \checkmark | 1: Device initiated background ops. is enabled. | \sim | Uncheck All |
| 6 | fPurgeEnable | W | | \checkmark | 0: Purge operation is disabled. | ~ | |
| 8 | fPhyResourceRemoval | R/W | 1 | \checkmark | 1: The dynamic capacity operation shall commence upon device EndPointReset or | \sim | |
| 9 | fBusyRTC | R | 0 | | 0: Device is not executing internal operation related to RTC. | \sim | Read |
| 11 | fPermDisableFwUpdate | R/WO | 0 | \checkmark | 0: The UFS device firmware may be modified. | \sim | Classe |
| _ | | | | | | | Cione |

- Check the needed items for programming and set up the values.
- > Before setting up the values, please delete all the previous values first.
- > The unchecked items in the Buffer value setting will not be saved.
- Check Selected: Click on anywhere in the column that you want to select, and then it will check all the boxes at once after you click Check Select.
- Uncheck Selected: Click on anywhere in the column that you want to cancel, and then it will cancel the boxes at once after you click Uncheck Select.
- > Check All: Check all the columns at once.
- > Uncheck All: Cancel all the columns at once.
- **Read:** Read the values from the IC and show on the Chip Value column.
- Clone: It will copy the values in the Chip value column to the corresponding Buffer Value column.



File function can save the Buffer Values as CSV file, or import the CSV file to the Buffer Value.



4. Set up the batch operation mode for programming.

| PC Scan | | Ç Config | PROJ Save Prj | PROJ Load Prj | Copy Function | | R | |
|---------------------------|------------------------|---|--|---|---|----------------|-----------------------|-------|
| Config | | | | | | | | |
| Bate | ah Bata | ch Setting | B | | | | | |
| Batc Descri Attribu | h h ptors tes | Ba Write Flat Write Der Wipe LUI Blank che Program Checksur | atch Operatio g ribute scriptor N eck(EngMode chip n verify | n :Only) | >> | | Operation Sele | ected |
| | IS ME | Skip Cl Blank o Batch Se Non PSA(O ROM | heck ID theck the part etting EXT e CopyModeO 1Burst(SAMSU | tition which no nly)(EPV) JNG,CopyMod | t loaded file while doin deOnly)(PV,Data will be | g verification | (Verify All) re PV | |

Basic Options

- Write Flag: Program and verify Flag register.
- Write Attribute: Program and verify Attribute register.
- Write Descriptor: Program and verify Descriptor register.
- Wipe LUN: Delete all Partitions' data
- Blank check (EngModeOnly): Blank check and it can only be executed in the engineering mode.
- **Program chip:** Program the Partition.
- **Checksum verify:** Verify the Partition.

Advance Option

- Skip Check ID: Not execute ID check
- PSA: Activate PSA programming function. The Batch mode will be fixed to EPV, and only can run in the copy mode.
- > If you have any concerns about the UFS PSA application methods, please contact with the IC supplier.



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5. Save as Project Folder.

| PROJ Save Prj Lo | PROJ pad Prj | Copy Function | | R | | |
|---------------------|-----------------|----------------|---|------|---------------|-----------------|
| Save NuProg+ | Project (Name | e a folder) | | | | × |
| Location: | DESKTOP\PRJ | I\中文目錄\UFS\ | | | ~ | Ŷ |
| | Name | | | Size | Date Modified | |
| PC | | | | | | |
| | | | | | | |
| Desktop | | | | | | |
| | | | | | | |
| | | | | | | |
| | | | | | | |
| | | | | | | |
| | < | | | | | > ^{ck} |
| | FileName: | UFS_Test_PRJ | 2 | ~ | OK | |
| | FileType: | All Files(*.*) | | ~ | Cancel | |

Name the project and it will generate a Project Folder after clicking OK, which will save all the required settings and content for programming. Please name the project by English character, number and underscore (_), and do not use space or other symbols.

| 名稱 ^ | 修改日期 | 類型 | 大小 |
|----------------------|----------------|----------------|----------|
| 🕸 cart_ufs_7c.bin | 2021/2/22下午 0 | BIN 檔案 | 3,737 KB |
| 🕸 fast_crc_table.bin | 2021/2/22 下午 0 | BIN 檔案 | 128 KB |
| LUN0.dpdat | 2021/2/22 下午 0 | DPDAT 檔案 | 0 KB |
| LUN0.dpmap | 2021/2/22下午 0 | DPMAP 檔案 | 0 KB |
| NuProgPlus_UFS.vec | 2021/2/22 下午 0 | VEC 檔案 | 5 KB |
| 🕬 ramf_ufs.bin | 2021/2/22 下午 0 | BIN 福案 | 32 KB |
| UFS_Test_PRJ.npprj | 2021/2/22 下午 0 | DediProg NuPro | 25 KB |



5.4 The Copy Mode of eMMC_UFS Client (For eMMC & UFS IC only)

After creating a project folder, save it to the computer that the other programmer is connected, and click Run, and then there will be three options.

- **Start From Buffer Memory**: Select a project folder from the programmer and execute.
- Start From Project Folder: Select a project folder from the computer, download the selected project folder to the programmer and execute. And then the next time using this, then you can just select the Start From Buffer Memory to save the loading time.
- **Scan Master Chip and Download**: Read the master IC and generate a project folder according to the assigned socket's location. The project file will be saved to the programmer and execute directly.





5.4.1 Start From Buffer Memory

Select the project file name for execution.

| PROJ ave Prj | PROJ j Load Prj | 1 Run Stop Sta | A rt Copy | [Powered by 🖪 |
|------------------------|--|---|---------------------------|---|
| C Info | | Select Project | > | < |
| | Site #3A Pass: 0 Fail: 0 N N 0.0% Site #3B Pass: 0 Fail: 0 N N 0.0% | 08GB_V 2 08GE_E 08GE_P 08GE_PV 4J1ZD_E 4J1ZD_PV 4J1ZD_V 8G_PPP 8G_PV 9C1CETE_P | ~ | I Version:3.16.13 Version:3.16.13.8 CreateTime:2021-04-2. |
| 1 | Site #7A Pass: 0 Fail: 0 | Project name: | OK Cancel | |
| | N 0.0% | N 0.0% | exFAT Free Size: 26.0(GB) | |

After it is finished, it will show "Select project success" in the Log window, meanwhile, the Run icon will turn grey, and the Stop, Start Copy will be available.

| Advance Help IC Confi | g | | | | | |
|---|---|--|--|---|---|---|
| Detect Load | Copy Scan Bu | ffer Config Save | Proj Prj Load Prj | Copy Function Run Stop | ↓ <u>&</u> Start Copy | Powered by |
| ReadIC Blank | Wipe Program Veri | fy Auto Batch IC In | fo | | Log Window | |
| #01 FPGA Ver: 33 S/N: PPU000002 F/W Ver: 1.6.3 CY3 Ver: 1.0.13 Plinte Batch | Site #1A Pass: 0 Fail: 0 S 00% | Site #2A Pass: 0 Fail: 0 S 0.0% | Site #3A Pass: 0 Fail: 0 N 0.0% | DLE Site #4A Pass: 0 Fail: 0 | CY3: 1.0.13 FPGA: 33 Programmer[1 NVME Size: 46 | 5.0(GB) |
| N 0.0% | Site #1B Pass: 0 Fail: 0 S 00% | Site #2B Pass: 0 Fail: 0 S 00% | Site #3B Pass: 0 Fail: 0 N 0.0% | DLE Site #4B Pass: 0 Fail: 0 | exFAT Size: 40 exFAT Free Siz (1) 13:39:06:Progr (1) 13:39:06:Progr (1) 13:39:13:Set op | e: 26.0(GB) ammer[01]- Selecting project ammer[01]- Running project beration:extCSD,Checksum verify,(Skip bl |
| | Site #5A Pass: 0 Fail: 0 N 0.0% | Site #6A Pass: 0 Fail: 0 IDLE N 00% | Site #7A Pass: 0 Fail: 0 | DLE Site #8A Pass: 0 Fail: 0 | () 13:39:13:Optic () 13:39:13:Chip DLE | nBytes CheckSum :0x0 CheckSum :0x8F991B48 ammer[01]- Select project success. ammer[01].chip[1A] Wait Start |
| | Site #5B Pass: 0 Fail: 0 N 0.0% | Site #6B Pass: 0 Fail: 0 N 00% | Site #7B Pass: 0 Fail: 0 | DLE Site #8B Pass: 0 Fail: 0 | CLE | ammer[01],chip[2A] Wait Start ammer[01],chip[2B] Wait Start v g Chip 0x8F991B48 |
| ChinInfo Type: eMMC Manufact: Corestorage Size: 0x1d2800200 Package: FBGA153 PartNum: CNE5A0YG | ID: 43 4d 4d 32 00 42 ADP P/N1: ND-eMMC-FBGA152 ADP P/N2: ADP P/N3: -08GE | 47 38 3115130-0028 Succe Failur Total: | uction Statistics ss: 0 e: 0 0 | Batch Config Setting extCSD Checksum verify | Check Sum File Chec File Size Partitio | n File Name |
| Idle | | | | ProjectName:08 | GB_V.npprj | ProjectCheckSum:0x2982 |



Click "Start Copy" or press the Start Button on the programmer, then it will start programming on the socket sites that are installed with the socket adaptors. If the socket sites are not installed with the socket adaptor, then it will be in grey, which will be N/A. The result will show after finishing programming, and the corresponding lights will be on



5.4.2 Start From Project Folder

Load the project file.

| Detect | Load | РС Сору | PC Scan | Buffe | r Co | nfig Save | DJ PROJ Prj Load Prj | Copy Function | Stop | Start Copy | |
|--|---|-------------------------------|--------------|---------------------------------------|--|---------------|-------------------------|---------------|------|---------------|---|
| LE ReadIC | - Blank | Wipe | As a Program | Verify | - Al | Scan Chip (Se | lect a folder) | | | | × |
| | | | | | _ | Location: | DESKTOP\eMMC_ | test\ | | ~ | Ť |
| #11 NuPro PGA Ver: /W Ver: 5/N: | 0gPlus 1F 1.1.19 NPS00001 | Site #1 Pass: 0 Fail: 0 | | .E | f ite # 2 Pass: 0 Pail: 0 | PC | Name | | Size | Date Modified | |
| BLink | Start 🔶 | N Site #1 Pass: 0 | B | N S F | ite #2 | Nesktop | | | | | |
| | | Fail: 0 | N/A | R R R R R R R R R R R R R R R R R R R | ail: 0 | | | | | | |
| | | Site #5 Pass: 0 Fail: 0 | N/A | F | oite #(Pass: 0 Pail: 0 | | | | | | |
| | | N | 10% | N | _ | | < | | | | > |
| | | Site #5 Pass: 0 Fail: 0 | B N/A | S F | ass: 0 ail: 0 | | | | 2 | OK Cance | 4 |

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After it is finished, it will show "Download project files success" in the Log window, meanwhile, the Run icon will turn grey, and the Stop, Start Copy will be available.



5.4.3 Scan Master Chip and Download

First, select the programming site where the master IC is placed, and then open the function to scan the master IC and set up the related values.

| Scan Batch Setting | × | Copy Function | |
|-------------------------|--------------------------|----------------------------|---|
| Scan chip type | | e Z Run | Stop Start Copy Powered by |
| OUFS | | | |
| eMMC | | | |
| Scan size | | | Log Window |
| Scan full chip | | Site #4A | |
| ○ Scan limit size | | Pass: 0 | (1) 13:57:30:DediWare start up,Build Version:3.16.13 |
| GB | | | (i) 13:57:30:DediWare NuProgPlus Version:3.16.13.8 |
| | | N 0.0% | (i) 13:57:30:Config file version:3.00,CreateTime:2021-04-27 1 |
| Batch Setting | Batch Setting EXT | Site #4B | Type: NuProgPlus US |
| EPV | None | Pass: 0 | Firmware: 1.6.3 |
| ⊖ PV | O PSA-Auto(CopyModeOnly) | Fail: 0 N/A | CY3: 1.0.13 |
| OE | PSA-Manual(CopyModeOnly) | N 0.0% | FPGA: 33 |
| OP | | Site #84 | Programmer[1] |
| ΟV | | Pass: 0 | NVME Size: 465.0(GB) |
| Skin Check ID | | Fail: 0 🔨 | exFAT Size: 465.0(GB) |
| | | N 0.0% | exrAT Free Size: 26.0(GB) |
| | | Site #8B | |
| | | Pass: 0 | |
| Project Name: SCAN_DE | MO | Fail: 0 N/A | |
| Save project file to DC | | N 0.0% | Save Log Clear Log Chip 0x00 |
| Save project file to PC | | ics - Batch Config Setting | Check Sum |
| Project files path: D:\ | | | File Chec File Size Partition File Name |
| | | | |
| | OK | | < |

- Scan chip type: Master IC type \geq
- \geq Scan Size: Scan Range (Start from Address 0x0).
- \geq Batch Setting: Batch Operation Mode
- \triangleright Batch Setting EXT: PSA or Specific Batch Operation Mode



- Skip Check ID: The ID of the IC that are not going to be compared
- Project Name: Programming Project Folder name. Please name the project by English character, number and underscore (), and do not use space or other symbols.
- Save project file to PC: Save the project folder on PC at the same time

After it is finished, it will show "Scan finish success" in the Log window, meanwhile, the Run icon will turn grey, and the Stop, Start Copy will be available.



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5.5 eMMC_UFS Client Common Functions

5.5.1 The way to update the FW/FPGA

NuProgPlus programmers needs four FW/FPGA inside.

1. Baseboard FPGA

The file format is "Base Vxx.bin", for example Base V33.bin (General Client and eMMC UFS Client share the same Baseboard FPGA)

The version number and the update functions of the Baseboard FPGA in eMMC UFS Client



2. CY3 FW

The file format is "NuProgPlus_Vx.x.x.img", for example NuProgPlus_V1.0.13.img (General Client and eMMC UFS Client share the same CY3 FW)

| The version n | umber and the | update | e functions of | the CY3 FW in | eMMC_L | JFS Client |
|---------------|---------------|--------|----------------|---------------|--------|------------|
| | | | - | | | |

| #01 | | Advance | нер | Beld | | | | | |
|-----------|-----------|----------|--|---------------------------|---------------------|--------|---|--|--|
| FPGA Ver: | 33 | _ | Firmware Manual Update(For Experts Only) | | | | | | |
| S/N: | PPU000002 | × | Ν | laster FPGA Manual Update | e(For Experts Only) | | | | |
| CY3 Ver: | 1.0.13 | Detect | S | laver Firmware Manual Upo | late(All) | | | | |
| BLink | Batch | 4.0 | S | lave FPGA Manual Update(/ | 4II) | | | | |
| D | 0.0% | | | | | | | | |
| | | | | | | | | | |
| | | < | | | | - | > | | |
| | | FileNam | e: | | ~ | ОК | | | |
| | | FileType | e: | Firmware File(*.img) | ~ | Cancel | | | |

Switch the File Type to ".img" and select the FW for update

3. Dedicated NPPG FW for General Client

The file format is "NPPG x x x(DBG).bin" or "NPPG x x x(LCD).bin"

NPPG x x x(DBG).bin) is for the NuProgPlus programmers that are installed in the automated systems. (Tip: The last number of the version number will be an odd number.)

NPPG x x x(LCD).bin is for the manual NuProgPlus-U8/U16 programmers, which support Standalone mode. (Tip: The last number of the version number will be an even number.)



4. Dedicated NPPEU FW for eMMC_UFS Client

The file format is "NPPEU_x.x.x(TRACE).bin" or "NPPEU_x.x.x(LCD).bin".

NPPEU_x.x.x(TRACE).bin is for the NuProgPlus programmers that are installed in the automated systems. (Tip: The last number of the version number will be an odd number.)

NPPEU_x.x.x(LCD).bin is for the manual NuProgPlus-U8/U16 programmers, which support Standalone mode. (Tip: The last number of the version number will be an even number.)

The version number and the update functions of the NPPEU FW in eMMC_UFS Client

| #01 | Advance | Help | Beta | | |
|-----------------|----------|------|---|-----|------|
| FPGA Ver: 33 | - | F | irmware Manual Update(For Experts Only) | | |
| S/N: PPU000002 | <u></u> | N | Naster FPGA Manual Update(For Experts Only) | | |
| CY3 Ver: 1.0.13 | Detect | s | laver Firmware Manual Update(All) | | |
| BLink Batch | 1- | S | lave FPGA Manual Update(All) | | |
| D 0.0% | | | | | |
| | | | | | |
| | | | | | |
| | < | | | | > |
| | FileNam | e: | | 0 | К |
| | FileType | • | Binary File(*.bin) | Can | icel |

Switch the File Type to ".img" and select the FW for update



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5.5.2 Manage the eMMC/UFS PRJ on the NuProgPlus-U8/U16

NuProgPlus-U8/U16 programmers can store multiple eMMC/UFS PRJ, you can use the below functions to delete the selected PRJ or all the PRJ.

| Advance | Help | IC Config | |
|-----------------------------|--------|-----------|-----|
| Language > | | | > [|
| Log in | | | |
| General options | | | |
| Socke | et | | |
| NuProgPlus Existing Project | | 4 | |
| NuPr | ogPlus | Compare | o |

NuProgPlus File Browser

| Name | Size | Туре | ^ |
|-----------------------|--------|------------|---------|
| G4J1ZD_P | | Folder | |
| 4J1ZD_V | | Folder | |
| ₿4J1ZD_E | | Folder | |
| B 8G_PV | | Folder | |
| BG_PPP | | Folder | |
| BGB_VVV | | Folder | |
| X28E_PV | | Folder | |
| X28E_P | | Folder | |
| X28E_V | | Folder | |
| X28E_E | | Folder | |
| BDW5_PV | | Folder | |
| BDW5_P | | Folder | |
| | | Folder | |
| BDW5_E | | Folder | |
| AG2GESD_PV | | Folder | |
| AG@GESD_P | | Folder | |
| AG2GESD_V | | Folder | |
| APALBH_PV | | Folder | |
| APAI BH P | | Folder | ~ |
| < | | | > |
| | | | |
| Programmer Index: 1 ~ | Delete | Delete All | Refresh |

- Programmer Index: Select a programmer
- > Delete: Delete the selected project folder
- > Delete All: Delete all the project folders
- Refresh: Read the project folder again



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5.5.3 Format the Buffer Memory on the NuProgPlus-U8/U16

It can format the Buffer Memory of all connected NuProgPlus-U8/U16 at once.

Advance Help IC Config

| | ~ | Firmware Manual Update(For Experts Only) Master FPGA Manual Update(For Experts Only) |
|--|-----------------|---|
| | Detect | Slaver Firmware Manual Update(All) |
| 4 ReadI | | Slave FPGA Manual Update(All) |
| | | LCD Firmware Update |
| | | Download default vector table |
| | | Base FPGA Manual Update(For Experts Only) |
| | | Format Programmer's SD card |
| | #01 | Reset Programmer Order |
| FPGA Ve S/N: F/W Ver CY3 Ver: | FPGA Ve S/N: | Launch Calculater |
| | User Manual | |
| | CY3 Ver: | About DediWare |
| BLINK | | Datel |

5.6 General Client (For Flash/EEPROM and MCU only)

The software instruction for Flash, EEPROM, and MCU, please refer to the DediWare user manual.



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VI. Socket Adaptor Installation

How to install socket adaptor to a programmer?

There is a triangle at the bottom of the socket adaptor.



Install the socket adaptor according to the below figure. If the connections are normal, then it is ready for programming.





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Note:

- 1. Improper installation may cause the damages.
- 2. Pick up the IC directly by hand may produce dirt or statics which may cause errors during the programming process. Therefore, please use IC picker for pickups.



VII. Revision History

| Date | Version | Description |
|------------|---------|---|
| 2019/12/28 | 1.0 | Initial |
| 2021/06/22 | 1.1 | Update chapter 5 software installations and eMMC_UFS programming guide. |
| 2022/01/22 | 1.2 | Modify model name to NuProgPlus-U8/U16 |

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